

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CYPR-CD00185

Inventor(s):

Warren Snyder, Craig Nemecek and Bert Sullam

Application No.:

09/975,030

Group Art Unit:

2123

Filed:

10/10/01

Examiner:

Dwin M. Craig

Title:

EMULATOR CHIP-BOARD ARCHITECTURE AND INTERFACE

Commissioner of Patents

P. O. Box 1450

Alexandria, VA 22313-1450

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

Pat. No.

Pat. Title

**Grant Date** 

6,564,179

DSP EMULATING A MICROCONTROLLER

05/13/03

Please direct all correspondence concerning the above-identified application to the following address:

#### WAGNER, MURABITO & HAO LLP

Two North Market Street, Third Floor San Jose, California 95113 (408) 938-9060 Customer No: 45545

Respectfully submitted,

Anthony C. Murabito Reg. No. 35,295



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# Form 1449

# **U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	Α	6,564,179	05/13/03	Belhaj	703	26	07/26/99
	В						
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Foreign Patent or Published Foreign Patent Application

Examiner Initial		Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
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#### **Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication					
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Examiner	<u> </u>	Date Considered					

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.